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| | Printed Pages – 3 | | TEC-603 | | |
| | (Following Paper ID and Roll No. to be filled in your Answer Book) | | | | |
| | PAPER ID: 3093 | | Roll No. | | |
| | B.Tech. | | | | |
| _ | (SEM V | VI) EVEN SEI | MESTER THEORY EXAMINATION, 2009-2010 | | |
| | VLSI TECHNOLOGY AND DESIGN | | | | |
| | Time: 3 | Hours | | Total Marks : 100 | |
| | Note: Attempt all questions. | | | | |
| | 1. Attempt any four parts of the following: (4x5) | | | | |
| | (a) | | Define the term IC. List its types. How does analog ICs' differ from digital ICs'? | | |
| _ | (b) | connected are there | In Bipolar IC the transistor are suitable connected to act as diode. How many ways are there to connect transistor to form a monolithic diode? | | |
| | (c) | is directly | Show that thickness of oxide during growth is directly proportional to time for small oxidation time. | | |
| | (d) | observed o | Explain why extremely rapid growth is observed during wet oxidation compared to dry oxidation? | | |
| TEC-603 | | 1 | [Turn Over | | |
| uptuonline.com | | | | | |

A thin film capacitor has a capacita uptugnline.com 0.4 pF/mil². The relative dielectric constant of SiO₂ is 3.5. What is the thickness of SiO₂

layer in angstroms?

layer. Attempt any two parts of the following:

Explain the various techniques to form oxide

layer and describe the applications of oxide

- 2. (2x10=20)Why is diffusion needed to form a bipolar (a) transistor? Explain with each diffusion
 - step. What is the equilibrium concentration of Schottky defects in Si at 300 K?
 - Write short notes on:
 - (i) Annealing (ii) **Epitaxy**
 - What are photo resist materials? Describe (c) types of photo resists. What are the requirements of oxide used for a mask in photolithography?
 - Attempt any four parts of the following: 3. (4x5=20)What is the advantage of polysilicon gate (a)
 - Describe the operation of a basic MOS (b) inverter. Derive the expression for a pull up to pull down ratio for an N MOS inverter.

over metal gate? How is it fabricated?

- Define the threshold voltage and derive the (c) expression for V_{th} in a MOS transistor.
- Draw a CMOS inverter and explain its (d) transfer characteristics.
- (e) Realize the expression Y = A(D + E) + BCusing nMOS transistor.
- (f) Draw the circuit diagram of one stage of a dynamic CMOS register.

uptuonline.com (e)

(f)

(b)

uptuonline4com Attempt any two parts of the followinguptuon(inc.com)

- (a) Draw a stick diagram for a two input multiplexed latch, using the two transmission gate side by side.
- (b) Discuss the architecture of FPGA. How FPGA programmed?
- (c) (i) Sketch the circuit diagram of a ratio less MOS inverter and explain its operation.
 - (ii) Discuss the operation of a 4 cell MOS RAM.
- 5. Attempt any two parts of the following: (2x10=20)
 - (a) Describe the lambda design rules and layout methodology for CMOS circuit design.
 - (b) Discuss programmable logic array (PLA) with example of NMOS PLA.
 - (c) Write a short note on VLSI testing.